

PATENT

Attorney Docket No. SAM-0250

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Kim, *et al.*  
Serial No.: 10/038,299  
Filing Date: January 4, 2002  
Title: PHASE LOCKED LOOP CIRCUIT FOR A FRACTIONAL-N  
FREQUENCY SYNTHESIZER

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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February 08, 2002  
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Assistant Commissioner for Patents  
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REQUEST FOR CORRECTION TO FILING RECEIPT

Sir:

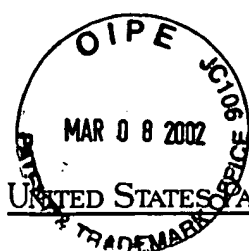
The Official Filing Receipt received in connection with the above-identified patent application is in error. The Applicant listed on the Filing Receipt is "Pil-Jae Jun, Suwon, KOREA, REPUBLIC OF".

The Applicants listed on the Filing Receipt should be "Sang-Young Kim, Kyunggi-do, KOREA, REPUBLIC OF and Pil-Jae Jun, Suwon, KOREA, REPUBLIC OF". A copy of the incorrect Filing Receipt with the requested change noted thereon is enclosed.

Respectfully submitted,

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/038,299	01/04/2002	2817	740	SAM-0250	9	16	1

CONFIRMATION NO. 8434

## FILING RECEIPT



\*OC000000007480368\*

Steven M. Mills  
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Date Mailed: 02/14/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s) Sang-Young Kim, Kyunggi-do, KOREA, REPUBLIC OF;  
Pil-Jae Jun, Suwon, KOREA, REPUBLIC OF;

## Assignment For Published Patent Application

Samsung Electronics Co., Ltd.;

## Domestic Priority data as claimed by applicant

## Foreign Applications

REPUBLIC OF KOREA 2001-15161 03/23/2001

If Required, Foreign Filing License Granted 02/14/2002

Projected Publication Date: 09/26/2002

Non-Publication Request: No

Early Publication Request: No

## Title

Phase locked loop circuit for a fractional-N frequency synthesizer

Preliminary Class

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